

Udoji Maratha Boarding Campus, Near Pumping Station, Gangapur Road, Nashik-13. <u>RSM POLY</u> Affiliated to MSBTE Mumbai, Approved by AICTE New Delhi, DTE Mumbai & Govt. of Maharashtra, Mumbai.

Subject :- Applied Electronics (22329)





Chapter	Name of chapter	Marks With	
110.			
1	Low power Amplifier	20	
2	High Power Amplifier	22	
3	Feedback Amplifier		
4	Waveform Generation	19	
5	IC voltage regulators and SMPS	19	
	Total Marks :-	100	

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BOARD THEORY PAPER PATTERN FOR AEL(22329)

Q.1		Attempt any FIVE5*2=10		
	a)	State classification of Amplifiers.		
	b .)	Define the terms related to tuned amplifier.		
	b) (i) Resonant Frequency (Fr) (ii) Q Factor			
	c)	State the need of multistage amplifier.		
	d)	List the types of power amplifiers.		
	e)	List advantages of negative feedback (any four)		
	f)	Define :(i) Sweep time (ii) Retrace time.		
	g)	State fixed voltage regulator IC'S.		
Q.2		Attempt any THREE3*4=12		
	a)	Sketch circuit diagram of RC coupled single stage CE amplifier. State the function of		
		each component.		
	b)	Describe the working of single stage class A amplifier with circuit diagram.		
	c)	Explain principle of feedback amplifier.		
	d)	Draw circuit diagram of RC phase shift oscillator and state its working.		
Q.3		Attempt any THREE3*4=12		
	a)	Sketch circuit diagram of common source FET Amplifier. State working principle of it.		
	b)	Explain the term crossover distortion. State methods to overcome it.		
	c)	Compare positive feedback and negative feedback on the basis of:(i)		
		Gain(ii)Bandwidth(iii) Phase shift(iv) Stability		



	d)	Draw block diagram of SMPS. State its working principle.		
Q.4		Attempt any FOUR 3*4=12		
	9)	Calculate Resonant frequency of single tuned amplifier, if inductor $L = 10mH$		
	<i>a)</i>	and connector $C = 4.7$ mf of tank circuit		
		An amplifier has gain 'A' of 300 without feedback output impedance is		
	b)	An amplifier has gain AV of 500 without recuback, output impedance is		
		IKW. If negative feedback with feedback factor 0.03 is introduced in the		
		circuit then calculate the gain with		
		feedback and output impedance of this feedback amplifier.		
	c)	Describe miller sweep generator circuit with neat input output waveforms.		
	d)	Draw block diagram of IC 723 regulator. State the working principle of IC 723.		
	e)	Compare RC coupled, transformer coupled, direct coupled amplifier on the basis of (i)		
	C)			
0.5		Type of coupling(ii) Frequency response(iii) Gain(iv) Application		
Q.5		Attempt any TWO 2×6=12		
	a)	Compare class A, class B, class AB, and class C amplifier.		
	b)	i) List different types of feedback amplifiers.		
		ii)List the advantages of negative feedback over positive feedback.		
	c)	i)Compare amplifier and oscillator.		
		ii)State use of heat sink.		
Q.6		Attempt any Two 2*6=12		
	a)	i)Explain with sketch the working of class B push pull amplifier.		
		ii)Draw miller sweep generation and give its applications.		
	b)	i)Explain the working principle of crystal oscillator with diagram.		
		ii)Describe with help of circuit diagram working of class A power amplifier		
	c)	Sketch the circuit diagram for dual voltage regulator using IC 78XX and 79XX to obtain		
		±12 V output and explain.		

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CLASS TEST - I PAPER PATTERN

COURSE: - Applied Electronics (22329) PROGRAMME: - E & TC Engineering

Syllabus: -

Unit	Nome of the Unit	Course Outcome
No.	Name of the Omt	(CO)
1	Low power Amplifier	CO-329.1
2	High Power Amplifier	CO-329.2
3	Feedback Amplifier	CO-329.3

Q. 1	Attempt any FOUR4*2=8Marks	Course Outcome (CO)
a)	What are different types of amplifier coupling?	CO-329.1
b)	Define efficiency of power amplifier.	CO-329.2
c)	What is crossover distortion?	CO-329.2
d)	Draw single tuned RF amplifier.	CO-329.1
e)	What is feedback? List its types.	CO-329.3
f)	Explain effect of coupling, bypass capacitor on Bandwidth.	CO-329.1
Q. 2	Attempt any THREE3*4=12 Marks	
a)	Draw single stage CE amplifier and explain its working.	CO-329.1
b)	Compare class A, class B, class AB, and class C amplifier.	CO-329.2
c)	Draw and explain block diagram of voltage series	CO-329.3
	feedback.	
d)	Draw class A push pull amplifier and describe its operation.	CO-329.2

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CLASS TEST - II PAPER PATTERN

COURSE: - Applied Electronics (22329) PROGRAMME: - E & TC Engineering

Syllabus: -

Unit	Name of the Unit	Course Outcome
No.	Name of the Omt	(CO)
3	Feedback Amplifier	CO-329.3
5	Waveform Generators	CO-329.4
6	IC voltage regulators and SMPS	CO-329.5

Q.1	Attempt any FOUR 4*2=8Marks	Course Outcome (CO)
a)	Draw block diagram of regulated power supply.	CO-329.5
b)	State the barkhausen criteria	CO-329.4
c)	State important features of IC 317	CO-329.5
d)	Draw crystal oscillator.	CO-329.4
e)	Give disadvantages of negative feedback.	CO-329.3
f)	Draw time base generator diagram.	CO-329.4
Q.2	Attempt any THREE3*4=12 Marks	
a)	Explain working SMPS with neat diagram	CO-329.5
b)	Draw labelled RC phase shift oscillator and explain.	CO-329.4
c)	Draw emitter follower using transistor and justify type of f/b.	CO-329.3
d)	Draw and explain bootstrap generator.	CO-329.4



COURSE OUTCOME (CO)

COURSE: -APPLIED ELECTRONICS (22329)

PROGRAMME: -E&TC

CO.NO	Course Outcome	
CO-329.1	Use transistor as low power amplifier.	
CO-329.2	Use BJT as high power amplifier.	
CO-329.3	.3 Use BJT as feedback amplifier.	
CO-329.4	Use BJT as waveform generator.	
CO-329.5	Maintain IC voltage regulator and SMPS	



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1.Low Power Amplifier

..... Total Marks-16

Position in Question Paper

- Q.1 a) 2-Marks. Q.1 b) 2-Marks. Q.1 c) 2-Marks. Q.2 a) 4-Marks.
- (0.2 a) 4-Marks. (0.3 a) 4-Marks.
- (0.5 a) 4-Marks. (0.4 a) 4-Marks.
- (0.4 b) 4-Marks.

Descriptive Question

- 1. What are different types of coupling in amplifier
- 2. Give classification of amplifier draw single stage RC coupled CE amplifier
- 3. Draw single tuned RF amplifier
- 4. Explain the effect of coupling, bypass capacitance on bamdwidth
- 5. Draw transformer coupled RF amplifier
- 6. Draw and explain single stage CE amplifier
- 7. Draw frequency response and bandwidth of CE amplifier.
- 8. What is need of multistage amplifier and draw two stage CE amplifier.
- 9. Compare RC coupled amplifier and direct coupled amplifier.
- 10. Draw and explain double tuned amplifier along with it's frequency

response 11. What is neutralization in RF tuned amplifier and it's advantages.

12.Compare single, double and stagger tunned amplifier

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MCQ Question

(Total number of Question=Marks*3=16*3=48)

Note: Correct answer is marked with **bold**

- 1. A single stage transistor amplifier contains ____ and associated circuitry a) Two transistors c) Three transistor d) None of the above b) One transistor 2. The phase difference between the output and input voltages of a CE amplifier is a) 180° c) 90° d) 270° b) 0° 3. It is generally desired that a transistor should have _____ input impedance c) High a) Low b) Very low d) Very high 4. When an a.c. signal is applied to an amplifier, the operating point moves along ____ a) d.c. load line c) both d.c. and a.c. load lines b) a.c. load line d) none of the above 5. If the collector supply is 10V, then collector cut off voltage under d.c. conditions is _____ c) 2 V a) 20 V b) 5 V d) 10 V 6. In the zero signal conditions, a transistor sees ____ load a) d.c. c) both d.c. and a.c. b) a.c. d) none of the above 7. The input capacitor in an amplifier is the ____capacitor a) Coupling c) Leakage d) None of the above b) Bypass 8. The point of intersection of d.c. and a.c. load lines is called _____ c) Operating point a) Saturation point b) Cut off point d) None of the above 9. The slope of a.c. load line is ____ that of d.c. load line a) The same as c) Less than d) None of the above **b)** More than 10. The purpose of capacitors in a transistor amplifier is to _____ a) Protect the transistor c) Couple or bypass a.c. component b) Cool the transistor d) Provide biasing 13. In the d.c. equivalent circuit of a transistor amplifier, the capacitors are considered _____ c) Partially short a) Short d) None of the above b) Open 14. In a CE amplifier, voltage gain = $x R_{AC}/R_{in}$ a) α c) $(1+\beta)$ b) $(1 + \alpha)$ **d**) **B** 15. In practice, the voltage gain of an amplifier is expressed b) As a number a) As volts
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c) In db	d) None of the above	
16. If R_C and R_L represent the collector resistance and load resistance respectively in a single		
stage transistor amplifier, then a.c. load is		
a) $R_L + R_C$	c) $R_L - R_C$	
b) $\mathbf{R}_{\mathbf{C}} \parallel \mathbf{R}_{\mathbf{L}}$	d) R _C	
17. In a CE amplifier, the phase difference between w	voltage across collector load R _C and	
signal voltage is		
a) 180°	c) 90°	
b) 270°	d) 0°	
18. In the a.c. equivalent circuit of a transistor amplif	ier, the capacitors are considered	
a) Short	c) Partially open	
b) Open	d) None of the above	
19. The purpose of d.c. conditions in a transistor is to)	
a) Reverse bias the emitter	c) Set up operating point	
b) Forward bias the collector	d) None of the above	
20. An amplifier has a power gain of 100. Its db gain	is	
a) 10 db	c) 40 db	
b)20 db	d) None of the above	
21. In order to get more voltage gain from a transisto	r amplifier, the transistor used should	
have		
a) Thin base	c) Wide emitter	
b) Thin collector	d) None of the above	
22. The purpose of a coupling capacitor in a transisto	or amplifier is to	
a) Increase the output impedance of transistor		
b) Protect the transistor		
c) Pass a.c. and block d.c.		
d) Provide biasing		
23. The purpose of emitter capacitor (i.e. capacitor ad	cross R_E) is to	
a) Avoid voltage gain drop	c) Reduce noise in the amplifier	
b) Forward bias the emitter	d) None of the above	
24. If the input capacitor of a transistor amplifier is s	hort-circuited, then	
a) Transistor will be destroyed	c) Signal will not reach the base	
b) Biasing conditions will change	d) None of the above	
25. A CE amplifier is also called circuit		
a) Grounded emitter	c) Grounded collector	
b) Grounded base	d) None of the above	
26. In transistor amplifiers, we generally use cap	acitors.	
a) Electrolytic	c) Paper	
b) Mica	d) Air	
27. The output power of a transistor amplifier is more	e than the input power because the	
additional power is supplied by		

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a) Transistor	c) Collector supply V _{CC}
b) Biasing circuit	d) None of the above
28. A transistor converts	
a) d.c. power into a.c. power	c) high resistance into low resistance
b) a.c. power into d.c. power	d) none of the above
29. A transistor amplifier has high output imped	ance because
a) Emitter is heavily doped	
b) Collector has reverse bias	
c) Collector is wider than emitter or base	
d) None of the abov	
30. For highest power gain, one would use	configuration
a) CC	c) CE
b) CB	d) none of the above
31. CC configuration is used for impedance matc	hing because its
a) Input impedance is very high	c) Output impedance is very low
b) Input impedance is low	d) None of the above
32. A coupling capacitor is	
a) A DC short	c) A DC open and an AC short
b) An AC open	d) A DC short and an AC open
33. In a bypass circuit, the top of a capacitor is	
a) An open	c) An AC ground
b) A short	d) A mechanical ground
34. The capacitor that produces an AC ground ca	lled a
a) Bypass capacitor	c) DC open
b) Coupling capacitor	d) AC open
35. The capacitor of a CE amplifier appear to be	
a) Open to AC	c) Open to supply voltage
b) Shorted to AC	d) Shorted to AC
36. A single stage transistor amplifier contains	and associated circuitry.
a) Two transistors	c) Three transistors
b) One transistor	d) None of the above
37. The phase difference between the output and	input voltages of a CE amplifier is
a) 180°	c) 90°
b) 0°	d) 270°
38. It is generally desired that a transistor	should have input impedance
a) Low	c) High
b) Very low	d) Very high
39. When an AC signal is applied to an amplifier	, the operating point moves along
a) DC load line	c) Both DC and AC load lines
b)AC load line	d) None of the above
40. The input capacitor in an amplifier is the	capacitor.
a) Coupling	b) Bypass



c) Leakage	d) None of the above
41. The slope of AC load line is that	t of DC load line
a) The same as	c) Less than
b) More than	d) None of the above
42. To reduce the distortion of an amplifier	signal, you can increase the
a) Collector resistance	c) Generator resistance
b) Emitter feedback resistance	d) Load resistance
43. If the emitter-bypass capacitor opens, the	e AC output voltage will
a) Decreases	c) Remains the same
b) Increases	d) Equals zero
44. If the emitter-bypass capacitor shorts, the	e base DC voltage will
a)Decreases	c) Remains the same
b)Increases	d)Equals zero
45. When negative voltage feedback is applied	ed to an amplifier, its voltage gain
a) Is increased	c) Remains the same
b)Is reduced	d)None of the above
46. The value of negative feedback fraction i	is always
a) Less than 1	c) Equal to 1
b)More than 1	d)None of the above
47. A feedback circuit usually employs	network
a) Resistive	c) Inductive
b)Capacitive	d)None of the above
48. The gain of an amplifier with feedback is	s known as gain
a) Resonant	c) Closed loop
b)Open loop	d)None of the above



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2. High Power Amplifier

Position in Question Paper

- Q.1. d) 2-Marks.
- Q.2. b) 4-Marks.
- Q.5. b) 6-Marks.
- Q.6. a) 3-Marks
- Q.6. c) 3-Marks

Descriptive Question

- 1. Define efficiency of power amplifier
- 2. What is cross over distortion
- 3. Draw direct coupled class A amplifier
- 4. Give classification of power amplifier
- 5. Draw complementary symmetry class AB amplifier
- 6. Compare class A, B, AB, and C amplifier
- 7. Draw and explain class B push pull amplifier
- 8. Explain class C amplifier with neat diagram.
- 9. Describe why efficiency of class C amplifier is higher than other amplifier $\$
- 10.Draw and explain class AB push pull amplifier

MCQ Question

(Total number of Question=Marks*3=18*3=54)

Note: Correct answer is marked with **bold**

1. The output stage of a multistage amplifier is also called _____ a) Mixer stage c) Detector stage d)F stage b)Power stage 2. ____ coupling is generally employed in power amplifiers a) Transformer c) direct b)RC d)Impedance 3. A class A power amplifier uses ____ a) Two transistors c) One transistor d)None of the above b) Three transistors 4. The maximum efficiency of resistance loaded class A power amplifier is a) 5% b)50%

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c) 30%	d)25%
5. The maximum efficiency of transformer coupled	d class A power amplifier is
a) 30%	c) 80%
b)50%	d)45%
6. Classpower amplifier has the highest collect	or efficiency
a) C	c) B
b)A	d)AB
7. Power amplifiers handle signals compare to	to voltage amplifiers
a) Small	c) Large
b) Very small	d)None of the above
8. In class A operation, the operating point is gene	rally located of the d.c. load line.
a) At cut off point	c) At saturation point
b)At the middle	d)None of the above
9. Class C amplifiers are used as	
a) AF amplifiers	c) F. amplifiers
b) Detectors	d)None of the above
10. A power amplifier has comparatively $_\{\beta}$	
a) Small	c) Very large
b)Large	d)None of the above
11. The maximum collector efficiency of class B of	operation is
a) 50%	c) 55%
b)90%	d)78.5%
12. A 2-transistor class B power amplifier is comm	nonly called amplifier
a) Dual	c) Symmetrical
b)Push-pull	d)Differential
13. If a transistor is operated in such a way that ou	tput current flows for 60° of the input signal,
then it is operation	
a) Class A	c) Class C
b)Class B	d)None of the above
14. When a transistor is cut off	
a) Maximum voltage appears across transisto	r
b)Maximum current flows	
c) Maximum voltage appears across load	
d)None of the above	
15. A class A power amplifier is sometimes called	amplifier
a) Symmetrical	c) Reciprocating
b)Single-ended	d)Differential
16. Class operation gives the maximum disto	ortion
a) A	c) C
b)B	d)AB
17. The output stage of a multistage amplifier usua	ally employs
a) Push-pull amplifier	c) Class A power amplifier
b)Preamplifier	d)None of the above

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18. Low efficiency of a power amplifier results in		
a) Low forward bias	c) More battery consumption	
b)Less battery consumption	d) None of the above	
19. The driver stage usually employs		
a) Class A power amplifier	c) Class C amplifier	
b) Push-pull amplifier	d) None of the above	
20. If the power rating of a transistor is 1W and colle	ctor current is 100mA, then maximum	
allowable collector voltage is		
a) 1V	c) 20V	
b) 100V	d)10V	
21. When no signal is applied, the approximate collect	ctor efficiency of class A power amplifier	
is		
a) 10%	c) 25%	
b)0%	d) 50%	
22. What will be the collector efficiency of a power a	amplifier having zero signal power	
dissipation of 5 watts and a.c. power output of 2 watt	s?	
a) 20%	c) 40%	
b) 80%	d) 50%	
23. The output signal voltage and current of a power	amplifier are 5V and 200 mA; the values	
being r.m.s. What is the power output?		
a) 1 W	c) 4 W	
b) 2 W	d) None of the above	
24. The maximum a.c. power output from a class A p	ower amplifier is 10 W. What should be	
the minimum power rating of the transistor used ?		
a) 10 W	c) 5 W	
b) 15 W	d)20 W	
25. For the same a.c. power output as above, what sh	ould be the minimum power rating of	
transistor for class B operation?		
a) 10 W	c) 8 W	
b)4 W	d) None of the above	
26. The push-pull circuit must use operation		
a) Class A	c) Class B	
b) Class C	d) Class AB	
27. The class B push-pull circuit can deliver 100 W of	of a.c. output power. What should be the	
minimum power rating of each transistor?		
a) 20 W	c) 10 W	
b)40 W	d) 80 W	
28. Power amplifiers generally use transformer coupl	ling because transformer permits	
a) Cooling of the circuit	c) Distortionless output	
b) Impedance matching	d) Good frequency response	
29. Transformer coupling can be used in ampl	ifiers	
a) Either power or voltage	c) Only voltage	
b) Only power	d) None of the above	

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42. The Q-point is at cutoff for class operation	tion
a) A	c) C
b) B	d) AB
43. How many transistors must be used in a class B p	ower amplifier to obtain the output for
the full cycle of the signal?	1 1
a) 0	c) 2
b) 1	d) 3
44. Which of the following is (are) power amplifiers?	
a) Class A	c) Class C or D
b) Class B or AB	d)All of the above
45. Which of the power amplifiers is not intended prin	marily for large-signal or power
amplification?	
a) Class A	c) Class C
b) Class B or AB	d) Class D
46. By how much does the output signal vary for a cla	ass AB power amplifier?
a) 360°	c) Between 180° and 360°
b) 180°	d) Less than 180°
47 Which of the following describe(s) a power ampli	fier?
a) It can handle large power.	c) It does not provide much voltage gain
b) It can handle large current.	d) All of the above
48 The feedback resistor	
a) Increases voltage gain	c) Decreases collector resistance
b) Reduces distortion	d) Decreases input impedance
49. In an LC transistor oscillator, the active device is	· · · · · · · · · · · · · · · · · · ·
a) LC tank circuit	c) Transistor
b) Biasing circuit	d) None of the above
50. Permanent Magnetic speakers commonly used in	
a) Radio signal Tower	c) Transformer
b) Radio	d) All of the above
51. In an LC circuit, when the capacitor is maximum,	the inductor energy is
a) Mmmum	
b) Maximum	
d) None of the above	
52. The O-point is above cutoff or above x axis for cl	ass operation
a) A	c) C
b)B	d) AB
53 The class B push-pull circuit can deliver 100 W o	f a c output nower What should be the
minimum power rating of each transistor?	i a.e. output power. what should be the
a) 20 W	c) 10 W
b) 40 W	d) 80 W



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- 54. The most costly coupling is ____ coupling
 - a) RC
 - b) Direct

c) Impedanced) Transformer



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3. Feedback Amplifier

Position in Question Paper

Total Marks-12

- Q.1.e) 2-Marks.
- Q.2.a) 4-Marks.
- Q.3.d) 4-Marks.
- Q.4.b) 4-Marks.

Descriptive Question

- 1. What is feedback?
- 2. What are types of feedback?
- 3. What is sampling and mixing wrt amplifier
- 4. What is effect of negative feedback on gain and bandwidth?

- 5. Draw and explain block diagram of voltage series feedback
- 6. Draw and explain voltage shunt feedback
- 7. Draw and explain voltage series feedback
- 8. What is effect of negative feedback on input and output resistance
- 9. Draw and explain current shunt feedback
- 10.Draw and explain current series feedback
- 11. Which type of feedback present in emitter follower amplifier? Justify
- 12. Give example of current series feedback and justify it

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MCQ Question (Total number of Ouestion=Marks*3=12*3=36) Note: Correct answer is marked with **bold** 1. When negative voltage feedback is applied to an amplifier, its voltage gain a) Is increased c) Remains the same d) None of the above **b)**Is reduced 2. The value of negative feedback fraction is always a) Less than 1 c) Equal to 1 b) More than 1 d) None of the above 3. A feedback circuit usually employs network c) Inductive a) Resistive b) Capacitive d) None of the above 4. The gain of an amplifier with feedback is known as ____gain a) Resonant c) Closed loop d) None of the above b) Open loop 5. When voltage feedback (negative) is applied to an amplifier, its input impedance _____. c) Remains the same a) Is decreased b) Is increased d) None of the above 6. When current feedback (negative) is applied to an amplifier, its input impedance c) Remains the same a) Is decreased d) None of the above b) Is increased 7. Negative feedback is employed in ____ a) Oscillators c) Amplifiers d) None of the above b) Rectifiers 8. Emitter follower is used for a) Current gain c) Voltage gain d) None of the above b) Impedance matching 9. The voltage gain of an emitter follower is a) Much less than 1 c) Greater than 1 d) None of the above b) Approximately equal to 1 10. When current feedback (negative) is applied to an amplifier, its output impedance ____ c) Remains the same a) Is increased b) Is decreased d) None of the above 11. Emitter follower is a ____ circuit a) Voltage feedback c) Both voltage and current feedback d) None of the above **b)**Current feedback 12. If voltage feedback (negative) is applied to an amplifier, its output impedance a) Remains the same c) Is decreased b) Is increased d) None of the above 13. When a negative voltage feedback is applied to an amplifier, its bandwidth c) Remains the same a) Is increased b) Is decreased d) Insufficient data Prepared by Prof. P.G.Deshmukh (Department of E & TC Engineering) Page 20 of 33



14. An emitter follower has input impedance	
a) Zero	c) High
b)Low	d) None of the above
15. The output impedance of an emitter follower is	
a) High	c) Almost zero
b) Very high	d)Low
16. The gain of an amplifier with feedback is known a	as gain
a) Resonant	c) Closed loop
b) Open loop	d) None of the above
17.A Negative-feedback amplifier is an amplifier	
a) Magnetic	c) Electromagnetic
b) Electronic	d) None of the above
18. The voltage gain of an emitter follower is	
a) Much less than 1	c) Greater than 1
b)Approximately equal to 1	d) None of the above
19. The output impedance of an emitter follower is _	
a) High	c) Almost zero
b) Very high	d) Low
20. Emitter follower is used for	
a) Current gain	c) Voltage gain
b)Impedance matching	d) None of the above
21. If the feedback fraction of an amplifier is 0.01, the	en voltage gain with negative feedback is
approximately	
a) 500	c) 1000
b)100	d) 5000
22. Emitter follower is a circuit	
a) Voltage feedback	c) Both voltage and current feedback
b) Current feedback	d) None of the above
23. When a negative voltage feedback is applied to a	n amplifier, its bandwidth
a) Is increased	c) Is decreased
b) Insufficient data	d) Remains the same
24. When current feedback (negative) is applied to an	amplifier, its input impedance
a) Remains the same	c) Is increased
b) Is decreased	d) None of the above
25. The value of negative feedback fraction is always	S
a) Less than 1	c) Equal to 1
b) More than 1	d) None of the above
26. An emitter follower has input impedance	
a) Zero	c) High
b)Low	d) None of the above
27. If voltage feedback (negative) is applied to an am	nplifier, its output impedance
a) Remains the same	c) Is decreased
b) Is increased	d) None of the above



28. When current feedback (negative) is applied to an	amplifier, its output impedance	
a) Is increased	c) Remains the same	
b) Is decreased	d) None of the above	
29. Negative feedback is employed in		
a) Amplifiers	c) Oscillators	
b) Rectifiers	d) None of the above	
30. When voltage feedback (negative) is applied to a	n amplifier, its input impedance	
a) Is increased	c) Remains the same	
b) Is decreased	d) None of the above	
31. A feedback circuit usually employs network		
a) Capacitive	c) Inductive	
b) Resistive	d) None of the above	
32. If the output of an amplifier is 10 V and 100 mV	from the output is fed back to the input,	
then feedback fraction is		
a) 10	c) 01	
b) 1	d) 15	
33. When negative voltage feedback is applied to an a	amplifier, its voltage gain	
a) Is increased	c) Remains the same	
b) Is reduced	d) None of the above	
34. Permanent Magnetic speakers commonly used in		
a) Radio signal Tower	c) Transformer	
b) Radio	d) All of the above	
35 The feedback resistor		
a) Increases voltage gain	c) Decreases collector resistance	
h) Reduces distortion	d) Decreases input impedance	
36 An emitter follower has Output impedance	a) Decreases input impedance	
a) Zero	c) High	
h) Low	d) None of the above	



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4. Waveform Generation

Position in Question Paper

Total Marks-14

- Q.1.c) 2-Marks. Q.2.d) 4-Marks. Q.3.c) 4-Marks.
- Q.4.c) 4-Marks.
- Q.5.c) 3-Marks

Descriptive Question

- 1. What is oscillator and draw it's block diagram
- 2. State barkhausen criterion of oscillation
- 3. Classify oscillator in brief.
- 4. Design RC phase shift oscillator
- 5. Give any four application of oscillator
- 6. Draw ckt diagram of crystal oscillator
- 7. Draw and explain RC phase shift oscillator
- 8. Draw and explain RC phase shift oscillator using transistor and write formula for frequency
- 9. Draw and explain crystal oscillator and write it's 2 application
- 10.Compare amplifier and oscillator
- 11. Compare RC and crystal oscillator
- 12.A phase shift oscillator uses equal resistance of 1Mohm and equal capacitance of 68pf. At what frequency does circuit oscillate?
- 13.A phase shift oscillator has R=220ohm and C=500pf. Calculate frequency of sine wave generated by oscillator.

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MCQ Question

(Total number of Question=Marks*3=14*3=42)

Note: Correct answer is marked with **bold**

1. An oscillator converts	
a) c. power into d.c. power	c) mechanical power into a.c. power
b)c. power into a.c. power	d)none of the above
2. In an LC transistor oscillator, the active device is	
a) LC tank circuit	c) Transistor
b)Biasing circuit	d)None of the above
3. In an LC circuit, when the capacitor is maximum,	the inductor energy is
a) Minimum	
b)Maximum	
c) Half-way between maximum and minimum	
d)None of the above	
4. In an LC oscillator, the frequency of oscillator is _	L or C.
a) Proportional to square of	
b)Directly proportional to	
c) Independent of the values of	
d)Inversely proportional to square root of	
5. An oscillator produces oscillations	
a) Damped	c) Modulated
b)Undamped	d)None of the above
6. An oscillator employs feedback	
a)Positive	c) Neither positive nor negative
b) Negative	d)Data insufficient
7. An LC oscillator cannot be used to produce	frequencies
a) High	c) Very low
b)Audio	d) Very high
8. Hartley oscillator is commonly used in	
a) Radio receivers	c) TV receivers
b)Radio transmitters	d)None of the above
9. In a phase shift oscillator, we use RC section	18
a) Two	c) Four
b)Three	d)None of the above
10. In a phase shift oscillator, the frequency determine	ning elements are
a) L and C	c) R and C
b)R, L and C	d)None of the above
11. A Wien bridge oscillator uses feedback	, ,
a) Only positive	c) Both positive and negative
b) Only negative	d) None of the above
12. The piezoelectric effect in a crystal is	·
a) A voltage developed because of mechanical	stress
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b) A change in resistance because of temperature	
c) A change in frequency because of temperature	
d) None of the above	
13. If the crystal frequency changes with temperature	e, we say that crystal has
temperature coefficient	
a) Positive	c) Negative
b)Zero	d) None of the above
14. The crystal oscillator frequency is very stable du	e toof the crystal
a) Rigidity	c) Low Q
b) vibrations	d)High Q
15. The application where one would most likely find	a crystal oscillator is
a) Radio receiver	c) AF sweep generator
b) Radio transmitter	d)None of the above
10. Which of the following signals are generated by	a) Triangular wave
a) Square wave	d) Pulse were
17 In a Wign bridge oscillator for obtaining 160Hz t	frequency output what will be the
canacitor value if resistance is selected as 1KO?	frequency output what will be the
a) 10 µF	c) 1 nE
b) $1 \mathbf{\mu} \mathbf{F}$	d) 10 nF
18 In a Wien-bridge oscillator frequency of oscillat	tion and capacitor value has
a) Direct proportionality	c) Fouglity
b) Inverse proportionality	d) No relation
19. Which of the following device is a non-linear dev	vice?
a) Resistance	c) Op-amp
b) Diode	d)Capacitor
20. In RC phase shift oscillator, one R-C bridge prov	vides phase shift.
a) 300	c) 900
b) 600	d) 1800
21. Which of the following oscillators have higher st	ability at a higher frequency?
a) Wien-bridge oscillator	c) Crystal oscillator
b)RC phase shift oscillator	d)All of the mentione
22. Which of the following can be considered as osci	illation frequency controlling element in
RC phase shift oscillator?	
a) Resistance	c) Both resistor and capacitor
b)Capacitor	d) None of the mentioned
23. Which of the following represents the LC tuned of	circuit?
a) Wien bridge oscillator	c) Hartley oscillator
b)T oscillator	d)All of the mentioned
24. Which of the following can be used to produce a	square waveform?
a) Wien bridge oscillators	c) Hartley oscillator
b) T-oscillator	d) Multivibrators
25. Pulse wave and square wave are the same in all a	spect.

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	â. (18)
a) True	b)False
26. An oscillator converts	
a) c. power into d.c. power	c) mechanical power into a.c. power
b)c. power into a.c. power	d) none of the above
27.In an LC oscillator, the frequency of oscillator is _	L or C.
a) Proportional to square of	
b) Directly proportional to	
c) Independent of the values of	
d) Inversely proportional to square root of	
28.An LC oscillator cannot be used to produce	frequencies
a) High	c) Verv low
b) Audio	d) Very high
29. In a phase shift oscillator, the frequency determin	ing elements are
a) L and C	c) R and C
b) R. L and C	d) None of the above
30. The piezoelectric effect in a crystal is	
a) A voltage developed because of mechanical s	tress
b) A change in resistance because of temperature	
c) A change in frequency because of temperature	
d) None of the above	
31 The application where one would most likely find	a crystal oscillator is
a) Radio receiver	c) AF sween generator
h)Radio transmitter	d) None of the above
32 For an oscillator to properly start the gain around	the feedback loop must initially be
a) 1	the receiver roop must mittany be
h)Greater than 1	
c) Less than 1	
d) Equal to attenuation of feedback circuit	
33 The Ω of the crystal is of the order of	
a) 100	c) 5 0
b) 1000	d) More than 10 000
34 In an I C oscillator if the value of L is increased f	our times the frequency of oscillations is
a) Increased 2 times	c) Increased 4 times
h) Decreased 1 times	d) Decreased 2 times
35 Which of the following is not an example for non-	-sinusoidal oscillator?
a) Sourceth Congreters	a) Multivibrator
h) Blocking oscillators	d) Crustel acciletors
36 The sinusoidal oscillator is also called	u)Crystal oscillators
a) I C oscillator	
a) Le oscillator	d) Crystal assellators
27 Low frequency excillators have a frequency range	u) Crystar Uscillators
37. Low frequency oscillators have a frequency fallet	(1 Hz - 20 Hz)
a) 20 Hz - 20 Hz	$C_{J} = \Pi Z - 2UK \Pi Z$
D) ZU HZ - IUUK HZ	U) JU HZ - IUUK HZ

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38. High frequency oscillators have a frequency range	e of	
a) 300K Hz-2G Hz	c) 8k Hz-800K Hz	
b) 100k Hz-500k Hz	d) 4K Hz-1G Hz	
39. Which of the following oscillator cannot be used	in low frequency oscillations?	
a) Wein bridge oscillators	c) Colpitts oscillators	
b) RC phase shift oscillators	d) RC oscillators	
40. Which of the following oscillator is not using a fe	edback network for its oscillation?	
a) LC oscillator	c) Crystal oscillator	
b) RC oscillator	d)Relaxation oscillator	
41. The signal generator generally used in the laborat	ories is oscillator	
a) Wien-bridge	c) Crystal	
b) Hartely	d) Phase shift	
42. Quartz crystal is most commonly used in crystal of	oscillators because	
a) It has superior electrical properties	c) It is quite inexpensive	
b) It is easily available	d) None of the above	
43. In a Wien-bridge oscillator, if the resistances in th	ne positive feedback circuit are decreased,	
the frequency		
a) Remains the same	c) Increases	
b) Decreases	d) Insufficient data	
44. One condition for oscillation is		
a) A phase shift around the feedback loop of 1800		
b) A gain around the feedback loop of one-third		
c) A phase shift around the feedback loop of 0o		
d) A gain around the feedback loop of less than 1		
45. In Colpitt's oscillator, feedback is obtained		
a) By magnetic induction	c) From the centre of split capacitors	
b) By a tickler coil	d) None of the above	
46 is a fixed frequency oscillator		
a) Phase-shift oscillator	c) Colpitt's oscillator	
b) Hartely-oscillator	d) Crystal oscillator	
47.An important limitation of a crystal oscillator is		
a) Its low output	c) Less availability of quartz crystal	
b) Its high Q	d) Its high output	
48. Which of the following is not an LC oscillator?		
a) Hartley Oscillator	c) Crystal oscillator	
b) Colpitts oscillator	d) Clapp oscillator	
49. Which type of oscillators are used in timing eleme	ents?	
a) RC oscillator	c) Crystal oscillator	
b) LC oscillator	d) None of the above	
50. Which of the following oscillator is will give mos	st stable output oscillation frequency?	
a) Colpitts oscillator	c) Wein bridge oscillator	
b) Clapp oscillator	d) Crystal oscillator	
51.A second condition for oscillations is		

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a) A gain of 1 around the feedback loop b) No gain around the feedback loop	
c) The attention of the feedback circuit must be on	e_third
d) The foodback circuit must be capacitive	c-und
52 The crystal oscillator frequency is very stable due	to of the crystal
a) Digidity	c) Low O
a) Kighting	d) High O
52 A Wien bridge escillator uses feedback	a) High Q
a) Only positive	a) Both positive and possitive
a) Only positive	d) None of the choice
54 A Wise bridge equilator was feedback	d) None of the above
54. A wien bridge oscillator uses feedback	
a) Only positive	c) Both positive and negative
b) Only negative	d) None of the above
55.An oscillator differs from an amplifier because it _	
a) Has more gain	c) Requires no d.c. supply
b) Requires no input signal	d) Always has the same input
56. In an LC transistor oscillator, the active device is	
a) LC tank circuit	c) Transistor
b) Biasing circuit	d) None of the above
57.In an LC circuit, when the capacitor is maximum,	the inductor energy is
a) Minimum	
b) Maximum	
c) Half-way between maximum and minimum	
d) None of the above	
58. An oscillator producesoscillations	
a) Damped	c) Modulated
b) Undamped	d) None of the above
59. An oscillator employs feedback	
a) Positive	c) Neither positive nor negative
b) Negative	d) Data insufficient
60. Hartley oscillator is commonly used in	
a) Radio receivers	c) TV receivers
b) Radio transmitters	d) None of the above
61.In a phase shift oscillator, we use RC sections	
a) Two	c) Four
b) Three	d) None of the above
62. A time base should have	
a) non-linearity	c) linearity
b) ramp relationship	d) unit step relationship
63. Sweep rate is controlled by a	
a) capacitor	c) diode
b) resistor	d) inductor
64. Time domain oscilloscopes require	



a) sweep generator	c) amplifier
b) oscillator	d) rectifier
66. What is time base generator?	
a) Time measuring device	c) voltage generation device
b) generator	d) current generation device
67. Linear sweep moves spot	
a) Top to bottom	c) left to right
b) right to left	d) bottom to top
68. Time base generator controls	
a) Intensity of light	c) voltage magnitude
b) current direction	d) spot movement
69. Sweep generator works on the principle of	
a) Inductor	c) Resistor
b) Capacitor	d) diode
70. Time base generator circuit resembles a	
a) regulator	c) amplifier
b) rectifier	d) oscillator



5. IC voltage regulators and SMPS

Position in Question Paper

Total Marks-10

- Q.2.d) 4-Marks.
- Q.4.a) 4-Marks.
- Q.5.b) 3-Marks.
- Q.6.c) 6-Marks

Descriptive Question

- 1. Define stability factor.
- 2. State application of time base generators
- 3. Classify time base generators
- 4. State working principle of miller sweep generator
- 5. Draw voltage regulated power supply
- 6. What is load regulation and line regulation
- 7. State function of 78XX and 79XX
- 8. Draw pin diagram of IC 723 and name it.
- 9. Draw low voltage low current using LM317
- 10. Give classification of SMPS
- 11. What is sweep period and retrace period?
- 12.Draw and explain voltage regulated power supply
- 13.Explain the factors affecting the power supply
- 14.Draw and explain series voltage regulator
- 15.Draw and explain miller sweep generator
- 16.Explain bootstrap time base generator
- 17.Compare miller sweep and bootstrap circuit
- 18.Draw functional diagram f three terminal voltage regulator and explain each block
- 19.Draw positive 12 vlt power supply using IC 7812.
- 20.Explain negative fixed value 8 vlt supply using IC 7908
- 21. Draw functional block diagram of IC 723. Describe its working.
- Prepared by Prof. P.G.Deshmukh (Department of E & TC Engineering)

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- 22.State 4 features and applications of IC 723.
- 23.Draw and explain three terminal voltage regulator IC 317

MCQ Question (Total number of Question=Marks*3=10*3=30)

Note: Correct answer is marked with **bold**

- 1. What is IC 723?
 - a) A voltage regulator

c) A half-wave rectifier

b) A full-wave rectifier

d) A clipper

- 2. What is line regulation?
 - a) The process of keeping Zener diode voltage constant inspite of changes in AC supply
 - b) The process of keeping load voltage constant irrespective of the fluctuation in AC supply or the line voltage
 - c) The process of keeping load voltage constant irrespective of fluctuation in load current
 - d) The process of keeping Zener current constant irrespective of fluctuation in AC suppl
- 3. What is load regulation?
 - a) The process of keeping the load voltage constant irrespective of any change in AC supply

b) The process of keeping the load voltage constant irrespective of variations in load current

- c) The process of keeping load voltage constant irrespective of variations in source current
- d) The process of keeping load current constant irrespective of variations in AC supply
- 4. Which is not considered as a linear voltage regulator?
 - a) Fixed output voltage regulator
 - b) Adjustable output voltage regulator
- c) Switching regulator d) Special regulator
- 5. Which type of regulator is considered more efficient?
 - a) All of the mentioned

- c) Fixed output regulator
- d)Switching regulator

d) None of the mentioned

c) 12V

d)-12V

- 6. Which of the following is not a characteristic of adjustable voltage regulators? c) Increased reliability
 - a) Non-versatile

b) Special regulator

- b) Better performance
- 7. The 7812 regulator IC provides _____. a) 5V
 - b)-5V

8. Voltage regulators keep a constant _____ output voltage when the input or load varies within limits.

a)Dc c) Ripple b)Ac d) none 9. The 7805 regulator IC provides _____. c) 12V a)5V d)-12V b)-5V

10. What is the range of the voltage level of the LM317 adjusted voltage regulator? Prepared by Prof. P.G.Deshmukh (Department of E & TC Engineering) Page 31 of 33



a) 0 V to 5 V	c) –5 V to –24 V
b) 1.2 V to 37 V	d) 5 V to 24 V
11. The 7912 regulator IC provides	
a) 5V	c) 12V
b)-5V	d)-12V
12. What is the typical dropout voltage for the 7812 f	ixed positive voltage regulator?
a) 5V	c) 12V
b)-5V	d) -12V
13. What is the ratio of the period of the output voltage	ge to the period of the input voltage in a
full-wave rectifier?	
a) 0	c) 1
b)0.5	d) 2
14. The regulator is less efficient than the _	type, but offers inherent short-
circuit protection.	
a) Series, shunt	c) Series, series
b) Shunt, series	d) Shunt, shunt
15 regulation can be defined as the percen	tage change in the output voltage for a
given change in load current.	
a) Load	c) Both a and b
b)Line	d) none
16. How many diodes conduct in the full-wave bridge	e rectifier while the capacitor is being
charged?	
a) 2	c) 1
b)4	d) none
17. Voltage regulation requires	
a) only line regulation.	c) a constant load.
b) only load regulation.	d)load and line regulation
18. For what range of fixed regulated voltages do the	series 78xx regulators provide
regulation?	
a) +5 V to +24 V	c) –5 V to –24 V
b)+5 V to +24 V	d) None of the above
19 regulation can be defined as the percent	tage change in the output voltage for a
given change in the input voltage.	
a) Load	c) Both
b)Line	d) none
20. What is full form of SMPS ?	
a) Switch Mode Power Supply	c) Storage Mode Power Supply
b) Simple Mode Power Supply	d) Storage Mode Power Shortage
21. SMPS is used for	
a) obtaining controlled ac power supply	
b) obtaining controlled dc power supply	
c) storage of dc power	
d) switch from one source to another	

22. Choose the incorrect statement.	
a) SMPS is less sensitive to input voltage variation	18
b) SMPS is smaller as compared to rectifiers	
c) SMPS has low input ripple	
d) SMPS is a source of radio interference	
23. In an unregulated power supply, if load current in	creases, the output voltage
a) Remains the same	c) Increases
b)Decreases	d) None of the above
24.In an unregulated power supply, if input a.c. voltage	ge increases, the output voltage
a) Increases	c) Remains the same
b) Decreases	d) None of the above
25. Power supply which has voltage regulation of	_ is unregulated power supply
a) 0 %	c) 10 %
b) 5 %	d)8%
26. An ideal regulated power supply is one which has	voltage regulation of
a)0%	c) 10%
b) 5%	d) 1%
27. A Zener diode utilises characteristic for volt	age regulation
a) Forward	c) Both forward and reverse
b)Reverse	d) None of the above
28. Zener diode can be used as	
a) c. voltage regulator only	
b)c. voltage regulator only	
c) both d.c. and a.c. voltage regulator	
d) none of the above	
29. A Zener diode is used as a voltage regulating	g device
a) Shunt	c) Series-shunt
b) Series	d) None of the above
30. Another name for Zener diode is diode	
a) Breakdown	c) Power
b) Voltage	d) Current
31. For increasing the voltage rating, zeners are connected as a senergy of the s	ected in
a) Parallel	c) Series
b) Series-parallel	d) None of the above
32. In a Zener voltage regulator, the changes in load of	current produce changes in
a) Zener current	c)Zener voltage/Zener current
b)Zener voltage	d)None of the above